## Call for papers

# **ISDCS 2021**

## Call for papers







Hiroshima University Higashi-hiroshima, Japan







#### **General Co-chairs**

Shinji Kaneko, Hiroshima University Hafizur Rahaman, IIEST Shibpur

#### **Program Co-chairs**

Takahiro lizuka, Hiroshima University Chandan Giri, IIEST Shibpur

#### **Program Committee**

Bhargab Bhattacharya, ISI Kolkata
Partha Bhattacharyya, IIEST Shibpur
Sanatan Chattopadhaya, Calcutta University
Debaprasad Das, Assam Central University
Sudeb Dasgupta, IIT Roorkee
Koh Johguchi, Shinshu University
Malay Kule, IIEST Shibpur
Nillohit Mukherjee, IIEST Shibpur
Tapas Kumar Maiti, DAIICT
Yutaka Sasaki, Hiroshima University
Manodipan Sahoo, IIT Dhanbad
Garrett Schlenvogt, Silvaco
Takao Yamamoto, Denso

#### **Publication Chair**

Dondee Navarro, Hiroshima University

#### **Secretaries**

Sandip Bhattacharya, Hiroshima University

### **Advisory Board**

Shinichi Tate, Hiroshima University Parthasarathi Chakrabarty, IIEST Shibpur





Courtesy of Hiroshima Prefecture

**SPONSORED BY** 



ILDP for Developing Innovators Transforming Advanced Technology to Social Goals, Hiroshima University ISDCS focuses on the recent innovations of Devices, Circuits and Systems highlighting the strong foundation in methodology and the integration of multidisciplinary approaches. Globally innovating scientists, researchers and engineers, who change the ways in which devices and circuits are understood, optimized, and leveraged in a variety of systems and applications, are cordially invited to contribute their latest results.

### ISDCS Symposium (March 3<sup>rd</sup> – 5<sup>th</sup>, 2021)

**Scope:** Original contributions are solicited on topics covered under broad areas such as (but not limited to):

- Physics, Analysis and Modeling of Devices for Different Applications
- Photonics and Optoelectronics of Advanced Materials
- Digital and Analog Circuits and Their System Applications
- Neural Networks & Neuromorphic Circuits and Systems
- · Circuit Testing and Verifications
- IoT Circuits and Systems
- Al Circuits for Machine Learning Systems
- Beyond CMOS Circuits and Hybrid systems
- Intelligent Systems and Robotics
- Environment Electronics and Their Applications
- Visual Communications & Multimedia Signal Processing

The Program Committee invites original, unpublished as well as review paper submissions on the above topics.

- Paper submissions should be complete manuscripts, not exceeding 4 pages abstract (including figures, tables, and bibliography) in a <u>standard IEEE two-column format</u>.
- Author(s) must submit a final camera-ready version of these papers for inclusion in the proceedings.
- Accepted papers will be submitted for inclusion into **IEEE Xplore**.
- Each accepted contribution must have at least one full paid registration by the time the camera-ready paper is submitted for inclusion in the proceedings.
- The committee reserves the right to remove from IEEE Xplore papers not presented at the symposium.

Abstract Submission: link: http://www.isdcs2021.hiroshima-u.ac.jp/

#### Important Dates

Abstract Submission Deadline: 18th December 2020

Notification of Acceptance: 31st January 2021 Camera Ready Submission: 15th February 2021 Program Announcement: 22th February 2021